Component Architecture/Design Diagram JOINT PLANNING EXECUTION TOOLKIT

JPET

(Provide hardware/software allocation diagram illustrating component modules, platforms they run on, internal and external interfaces, databases, and APIs supported)

Not Available at this time

Component Details

JPET

Component Design

COTS/GOTS DEPENDENCIES: (to include information and other applications)

JPET requires a COTS Objectivity (object oriented) database license for TARGET to function. An Orbix license is required if TARGET is to interoperate with a JTF-ATD Plan Server. The map system in TARGET (current version MATT 3.7p1) requires an Orbix license. TPEDIT and FMEDIT require an (COTS) Oracle relational database license. TPEDIT requires a XPort license.

HARDWARE/OS DEPENDENCIES:

JPET operates on a Sun Sparc 10, 20 and UltraSparc workstations, Hewlett Packard TAC-3 and TAC-4 workstations and RDI Powerlite portable computers. The minimum memory requirements are 128 MB and 4 GB Hard Disk storage. JPET operates under the Solaris 2.4, 2.5 and HPUX 9.0.7 operating systems.

DESIGN METHODS/REPRESENTATION:
DESIGN STANDARDS: (References)
DESIGN ARTIFACTS (INCLUDING CASE TOOL DATABASES): (Available and Planned)
INTERFACE DOCUMENTATION: (Internal & external and when it will be updated)
DATABASE VIEW DOCUMENTATION: (References and when it will be updated)
Component Implementation LANGUAGES USED:C++, ANSI-C, LISP, tcl/tk

Coding Standards: (References)
Software Size: (Planned or Actual)200MB plus map data
USER INTERFACE TECHNOLOGY:X11R5, Motif 1.2
REUSED MODULES: